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APPLICATION
FOR
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TITLE: CIRCUIT BOARD CONSTRUCTION

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CIRCUIT BOARD CONSTRUCTION

Field of the Invention:

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The present invention relates to circuit boards and, more particularly, to a new
multilayered circuit board structure that electrically connects the laminated circuit board
layers by filling the via through holes of the inner power core layer with conductive
adhesive, the conductive power core vias making contact with the metal pads of the
conductive vias of the outer signal layers, upon lamination.

BACKGROUND OF THE INVENTION

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The processes for manufacturing circuit boards are varied, with each method
trying to achieve a more cost effective way to achieve a highly complex circuit structure.

In United States Patent No. 5,920,123, issued on July 06, 1999, to Moden for
MULTICHIP MODULE ASSEMBLY HAVING VIA CONTACTS AND METHOD OF

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MAKING THE SAME, a circuit board construction is illustrated wherein traces of electrically conductive materials are deposited in vias of a circuit layer by stencil printing or syringe dispensing techniques. Bonding pads disposed on an adjacent layer align with the vias, and make electrical contact with the conductive materials deposited therein. The via and pad connections, however, do not teach or suggest via/core connections, as illustrated in the current invention.

In United States Patent No. 5,157,477, issued to Chance on October 20, 1992 for MATCHED IMPEDANCE VERTICAL CONDUCTORS IN MULTILEVEL DIELECTRIC LAMINATED WIRING, assigned to a common assignee, a multiplane, laminated circuit board is shown wherein a plurality of conductive via holes are aligned with a flush or countersunk pad. The construction features electrical impedance matching within a core, but fails to address core to core connectivity, as taught by the present invention.

In United States Patent No. 5,574,630, issued to Kresge et al on November 12, 1996 for LAMINATED ELECTRONIC PACKAGE INCLUDING A POWER/GROUND ASSEMBLY, assigned to a common assignee, a printed circuit board sub-assembly is depicted, featuring in one embodiment thereof, a flexible substrate with via holes. The

structure features two multilayered, multi-density cores that are connected by a copper-to-copper pad electrical interface.

In United States Patent No. 5,442,144, issued to Chen et al on August 15, 1995 for MULTILAYERED CIRCUIT BOARD, assigned to a common assignee, a method of making a multilayered circuit board is illustrated. The method features solder plating via pads to each other by reflow temperature techniques during the lamination process. By contrast, the present invention presents a method of bonding, that uses electrically conductive adhesives at this interface.

In United States Patent No. 5,298,685, issued on March 29, 1994 to Bindra et al for INTERCONNECTION METHOD AND STRUCTURE FOR ORGANIC CIRCUIT BOARDS, assigned to a common assignee, polymeric subcomposites of a circuit board are interconnected by dendrites on specific areas of the copper pads.

In United States Patent No. 5,229,550, issued to Bindra et al on July 20, 1993 for ENCAPSULATED CIRCUITIZED POWER CORE ALIGNMENT AND LAMINATION, assigned to a common assignee, a structure and method is shown for making high density circuit boards. The cores of the circuit board are connected with

dendrites or solder.

5 The present invention comprises a multi-layered, laminated circuit board construction, wherein conductivity is transferred from signal core to signal core through a power core. The via through holes of the power core structure are filled with conductive adhesives, and then "B" stage cured. Upon lamination of all of the core layers, an electrical connection is achieved between the conductive via through holes of the outer signal layers when their metallic pads contact the conductive adhesive disposed in the vias of the inner power core layer.

In another embodiment of the current invention, the power core pads are covered with conductive adhesive. Electrical contact with the adjacent metallic pads of the conductive vias of the signal cores is achieved upon lamination of the inner and outer core layers.

SUMMARY OF THE INVENTION

15 In accordance with the present invention, there is provided an improved circuit board construction featuring a multilayered, laminated structure. Conductive adhesives

are used to provide electrical connectivity between outer signal core layers through an inner power core layer. The via through holes of the intermediate power core layer are filled with conductive adhesive, and then "B" stage cured. Upon the lamination of all the cores, a connection is achieved between the metallic pads of the conductive vias of the outer signal core layers with the conductive adhesive disposed in the vias of the inner power core layer.

In another embodiment of the current invention, the vias of the inner power core layer are covered at their surface with conductive adhesive pads. These conductive pads make electrical contact with the adjacent metallic pads of the via through holes carried by the outer signal core layers when the inner and outer layers are laminated.

It is an object of the present invention to provide an improved circuit board construction using conductive adhesives.

It is another object of this invention to provide a multilayered, laminated circuit board, wherein electrical connectivity between the outer signal core layers and the inner power core layer is achieved by using conductive adhesive to fill the vias of the power core layer.

BRIEF DESCRIPTION OF THE DRAWINGS

A complete understanding of the present invention may be obtained by reference to the accompanying drawings, when considered in conjunction with the subsequent detailed description, in which:

FIGURE 1 illustrates an exploded, sectional, schematic view of a first embodiment of the multilayered circuit board structure of this invention;

FIGURE 2 depicts an exploded, sectional, schematic view of a second embodiment of the multilayered circuit board structure of this invention; and

FIGURE 3 shows an exploded, sectional, schematic view of a third embodiment of the multilayered circuit board structure of this invention.

For purposes of brevity and clarity, like elements and components will bear the same numbering and designations throughout the figures.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Generally speaking, a multilayered construction of a circuit board is illustrated. Conductive adhesives are used to provide electrical connectivity between outer signal core layers and an inner power core layer in the preferred embodiment. The via through holes of the intermediate power core layer are filled with conductive adhesives, and then "B" stage cured. Upon lamination of all of the core layers, an electrical connection is achieved throughout the layers when the metallic pads of the outer signal layers make contact with the conductive adhesive-filled vias of the inner power core layer.

Now referring to FIGURE 1, a multilayered circuit board 10 of this invention is shown in exploded, sectional view. The circuit board 10 comprises two, outer, signal core layers 12, and a middle power core layer 14. It should be understood, however, that any number of layers 12, 14 can be used to create a multilayer board 10, and the invention is not meant to be limited to the layers shown herein for descriptive purposes only. The power core layer 14 comprises a plurality of via through holes 16 that are filled with a conductive adhesive 1, such as: ABLESTICK 8175. The adhesive is "B" stage cured.

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The circuit board 10 is fabricated by laminating the outer signal core layers 12 with the intermediate power core layer 14. An electrical connection is achieved when the layers 12 and 14 are laminated together. The connection results from the contact of the precious metal coated pads 3 of the conductive vias 15 of the signal core layers 12 with the conductive surfaces 2 of the conductive adhesive-filled vias 16 of the power core layer 14.

Referring to FIGURE 2, a circuit board 20, depicted in exploded, sectional view, illustrates a second embodiment of the invention. In this embodiment, conductive adhesive-filled via through hole surfaces 4 of power core layer 14 are slightly undercut. The undercut provides a larger contact area between the precious metal coated pads 3 of the conductive via through holes 15 of the signal core layers 12 and the conductive surfaces 4 of the via through holes 16 of the power core layer 14. The layers 12 and 14 are laminated under high heat and pressure.

Referring to FIGURE 3, an exploded, sectional view of a circuit board 30 depicts a third embodiment of this invention. In this embodiment, conductive adhesive is applied to precious metal coated pads 5 of the via through holes 16 of the power core layer 14, prior to lamination. Electrical connectivity is achieved between the conductive via

through holes 15 of the outer signal core layers 12 and the conductive via through holes 16 of the inner power core layer 14 after lamination.

Since other modifications and changes varied to fit particular operating requirements and environments will be apparent to those skilled in the art, the invention is not considered limited to the example chosen for purposes of disclosure, and covers all changes and modifications which do not constitute departures from the true spirit and scope of this invention.

Having thus described the invention, what is desired to be protected by Letters Patent is presented in the subsequently appended claims.

What is claimed is: